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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/612,712	06/30/2003	Richard S. Perry	884.941US1	3752
21186	7590	03/23/2006	EXAMINER	
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH 1600 TCF TOWER 121 SOUTH EIGHT STREET MINNEAPOLIS, MN 55402			NGUYEN, JIMMY	
			ART UNIT	PAPER NUMBER
			2829	

DATE MAILED: 03/23/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/612,712

Applicant(s)

PERRY, RICHARD S.

Examiner

Jimmy Nguyen

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 05 January 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1 - 8 and 15 - 27 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1 - 8 and 15 - 27 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

## DETAILED ACTION

### Response to Argument

The amendment filed 1/05/06 has been considered with the following effect;

The arguments are in mood of new ground of rejection.

### *Claim Rejections - 35 USC § 102*

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1 – 8 and 15 – 27 are rejected under 35 U.S.C. 102(e) as being anticipated by Stierman et al (US 2003/0218463 A1)

**As to claim 1**, Stierman et al disclose (fig 6) a test device comprising:

An element (18 connect to the pin 46, fig 3) having a surface for electrically contacting a first plane (54, paragraph 24); and

A probe (44) having a free end positioned in a second plane (signal plane) for electrically contacting the second plane (dut 8).

**As to claims 2, 26, 27**, Stierman et al disclose (fig 6) a test device comprising the test device of claim 1 wherein the element (18 connect to the pin 46, fig 3) having the surface to contact the first plane (54, paragraph 24) includes features for contacting a ground plane.

**As to claim 3**, Stierman et al disclose (fig 6) a test device comprising the length of the probe (52, 44) is greater than the length of the element (18 connect to the pin 46, fig 3) having a surface for contacting the first plane.

**As to claims 4, 5**, Stierman et al disclose (fig 3) the element (46) shield the device.

**As to claim 6**, Stierman et al disclose (fig 6) the test device of claim 1 wherein the element (18 connect to the pin 46, fig 3) that shields the probe (44) further comprises features for contacting a ground plane dimensioned to prevent interference from radio signals of a selected frequency.

**As to claim 7**, Stierman et al disclose (fig 6) the test device of claim 6 wherein the features for contacting a ground plane (54, paragraph 24) include a plurality of pointed peaks separated by valleys wherein the height of the peaks are dimensioned to prevent passage of radio signals of a selected frequency.

**As to claim 8,** Stierman et al disclose (fig 6) the element includes a cylindrical portion ( 52) surrounding a portion of the probe.

**As to claim 15,** Stierman et al disclose (fig 6) an electrical testing device comprising:  
a first test probe (18 connect to the pin 46, fig 3) adapted to contact electrical elements in a first plane (ground plane) ; and  
a second test probe (52) , the second test probe further comprising:  
an element (46,fig 3) having a surface for contacting a first plane (ground plane); and  
a probe (44) having a free end positioned in a second plane (plane contact dut 8).

**As to claim 16,** Stierman et al disclose (fig 6) the electrical testing device comprising  
a fixture having a nest adapted to receive an electrical device having elements to be tested on a first plane and on a second plane.

**As to claim 17,** Stierman et al disclose (figs 5, 6) the electrical testing device wherein the electrical device is a circuit board (8).

**As to claims 18, 23,** Stierman et al disclose (figs 5, 6) a method for testing dut comprising:

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Contacting a first pad (54) on dut located in a first plane (ground plane);

Contacting a second pad (48) on the dut (8) in a second plane (signal plane)

substantially simultaneously as contacting the first pad.

**As to claims 19, 24,** Stierman et al disclose (figs 5, 6) the first pad (54) and the second pad (48) are contacted from the same side of the dut (8).

**As to claims 20, 25,** Stierman et al disclose (figs 5, 6) the contacting the second pad (48) of dut 98) further comprises passing a probe (44) through a portion of a dut.

**As to claim 21,** Stierman et al disclose (figs 5, 6) the dut is a circuit board (8) having an electrical component is socket that physically holds the ic undetr test not shown) attached to a primary side of the circuit board (8) and wherein passing a probe (44) through a portion of the dut includes passing a probe through an opening in the circuit board.

**As to claim 22,** Stierman et al disclose (figs 5, 6) the electrical testing device of claim 15 further comprising a fixture having a nest adapted to receive an electrical device that includes:

a printed circuit board (8) further including:

a primary side (top side); and

a secondary side (bottom side);

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a component (not shown) having a main body, the component attached to the primary side (top side) of the printed circuit board (8) and further including a pad (48) attached to the main body of the component, the pad (48) positioned between the main body of the component and the primary side of the printed circuit board; a ground plane (ground 54) connection surface attached to the secondary side of the printed circuit board (8) , the printed circuit board having an opening therein positioned near the pad attached to the main body of the component.

### **Conclusion**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jimmy Nguyen whose telephone number is (703) 306-5858. The examiner can normally be reached on M-F from 9 to 5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ramitez Nestor, can be reached on 571-272-2034. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.


Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only.

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For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jimmy Nguyen

3/15/2006

  
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PRIMARY EXAMINER  
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03/20/06